

**mynatix**

AT A GLANCE



# mynatix



## ■ PUSHING LIMITS

Efficiency is critical in modern IT. With a novel approach to include unexploited code information, Mynatix makes software hardware-intelligent while unlocking new performance levels and significantly improving efficiency.

## ■ MAKING CODE ADAPTIVE

Our disruptive, physics-based technology enables software to understand its hardware, allowing code to autonomously adjust to any computing environment.

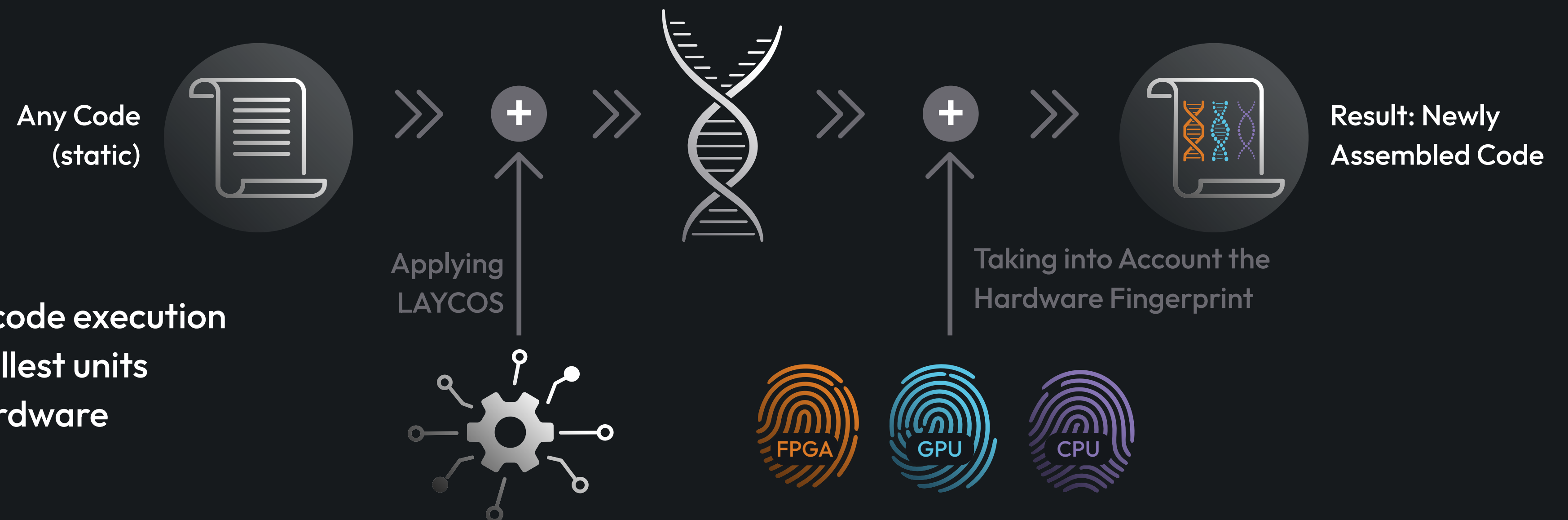




## ■ OPTIMIZING SOFTWARE

Software only achieves optimal results if code and hardware are perfectly matched. Today's computing environments are complex and fast changing. LAYCOS (Latency-Optimized Code Segmentation) considers all physical dependencies at the hardware and data levels when adapting code to platform, from microchip to supercomputer.

Our technology optimizes parallel code execution by breaking down code into its smallest units before distributing them across hardware (e.g. FPGAs, GPUs or CPUs).

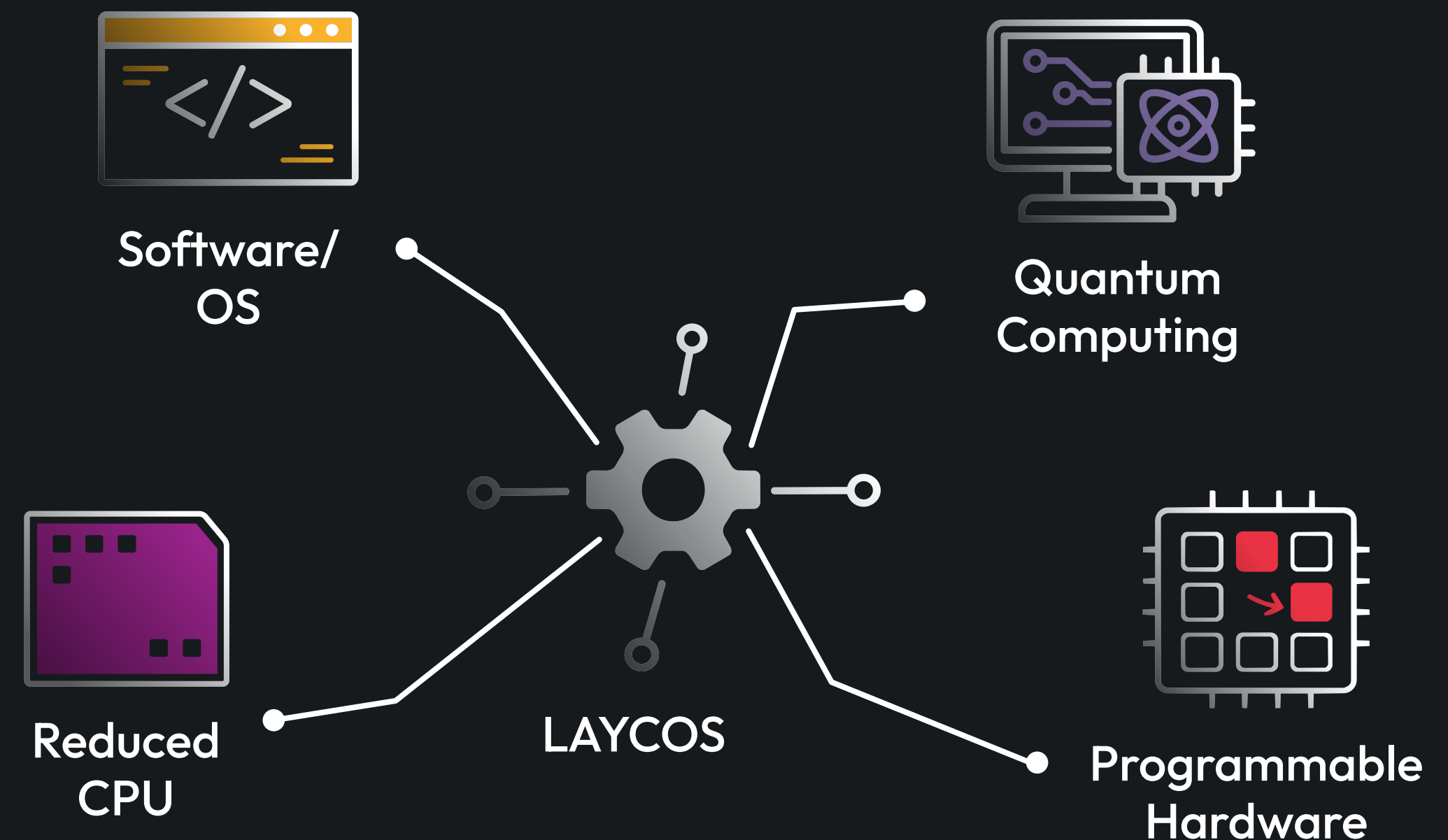




## ■ AUTOMATED SEGMENTATION

Achieving high-performance and energy-efficient implementations requires specific expertise and development time from the programmer or an AI agent. Our disruptive approach automates code optimization to any platform, saving time, costs, and energy.

Protected by international patents, our technology drives efficiency in all areas of computing, from software development to chip design and cloud computing.





## ■ CHALLENGES

### SOFTWARE/ OS

Due to missing software to hardware optimization, CPU utilization in today's data centers is only 12–18%.

### PROGRAMMABLE HARDWARE

FPGAs deliver unmatched efficiency for custom workloads, however, development time is 15 times longer for FPGAs than for an equivalent GPU implementation.

### REDUCED CPU

Up to 40% of a CPU's area and energy consumption is required for dynamic instruction scheduling.

### QUANTUM COMPUTING

Up to 1'000 physical qubits per logical qubit are needed only for error protection. Every gate saved reduces this overhead.



## ■ OUR APPROACH

### SOFTWARE/ OS

Mynatix makes software hardware-aware. Powered by LAYCOS, it identifies execution dependencies and enables parallel execution across heterogeneous systems. This reduces complexity, improves performance predictability, and eliminates the need for manual optimization.

### PROGRAMMABLE HARDWARE

Mynatix translates high-level software into hardware-ready implementations. LAYCOS segments code into executable units, enabling efficient FPGA and ASIC designs while reducing complexity and development time.

### REDUCED CPU

Mynatix shifts execution complexity from hardware to compilation. By exposing instruction-level parallelism, LAYCOS enables simplified CPU designs that are high-performing, have reduced chip area, and consume less energy.

### QUANTUM COMPUTING

Mynatix applies physics-based code segmentation to quantum computing. LAYCOS identifies execution dependencies and enables parallel circuit construction, which simplifies quantum programming and improves scalability.